

RoHS

COMPLIANT

**HALOGEN** 

FREE

## **Ultrafast Rectifier, 15 A FRED Pt®**



2L TO-220 FullPAK

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	15 A			
$V_{R}$	600 V			
V <sub>F</sub> at I <sub>F</sub>	0.85 V			
t <sub>rr</sub> (typ.)	60 ns			
T <sub>J</sub> max.	175 °C			
Package	2L TO-220 FullPAK			
Circuit configuration	Single			

#### **FEATURES**

- State of the art low forward voltage drop
- Ultrafast soft recovery time
- 175 °C operating junction temperature
- · Low leakage current
- Fully isolated package (V<sub>INS</sub> = 2500 V<sub>RMS</sub>)
- True 2 pin package
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION**

State of the art, ultralow  $V_F$ , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

#### **APPLICATIONS**

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		600	V
Average rectified forward current in DC	I <sub>F(AV)</sub>	T <sub>C</sub> = 120 °C	15	۸
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	200	Α
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	600	-	-	.,
Forward voltage V <sub>F</sub>	W	I <sub>F</sub> = 15 A	-	0.99	1.07	V
	I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	0.85	0.91		
Reverse leakage current I <sub>R</sub>	$V_R = V_R$ rated	-	0.01	15		
	'R	$T_J = 150  ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	6	100	μΑ
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	12	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH





<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
			$I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		60	110	
Reverse recovery time	+	$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	185	270	1
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	$I_F = 15 \text{ A},$ $dI_F/dt = 200 \text{ A/}\mu\text{s},$ $V_R = 390 \text{ V},$	-	210	-	ns
		T <sub>J</sub> = 125 °C		-	290	-	
Peak recovery current I <sub>RRM</sub>	I	T <sub>J</sub> = 25 °C		-	20	-	A
	IRRM	$T_{J} = 125  ^{\circ}\text{C}$ $V_{B} = 390  \text{V}$ .		26	-	^	
Reverse recovery charge Q <sub>rr</sub>	0	T <sub>J</sub> = 25 °C		-	2.2	-	μC
	T <sub>J</sub> = 125 °C		-	4.0	-	μΟ	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	3.7	4.3	
Thermal resistance, junction-to-ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W
Typical thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-	
Maiabt			-	2	-	g
Weight			-	0.07	-	oz.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style 2L TO-220 FullPAK		ETL1	506FP	•



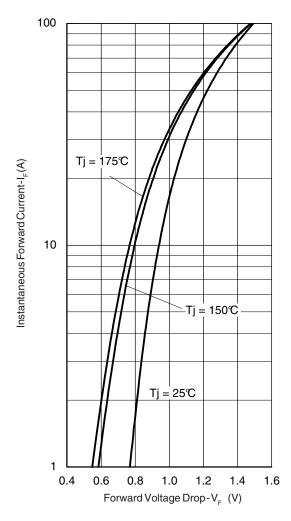


Fig. 1 - Typical Forward Voltage Drop Characteristics

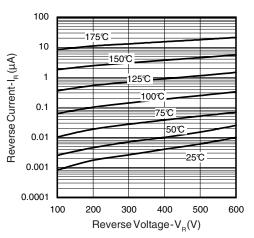


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

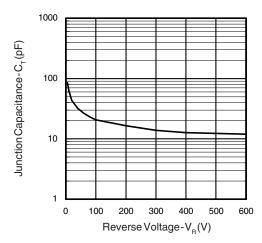


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

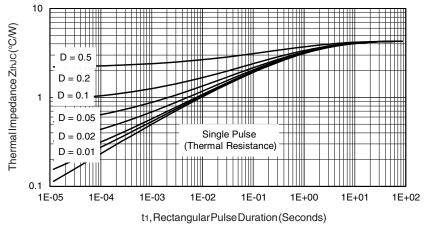


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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## Vishay Semiconductors

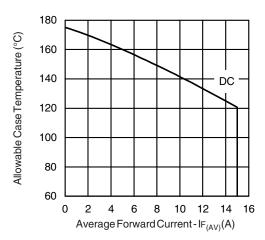


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

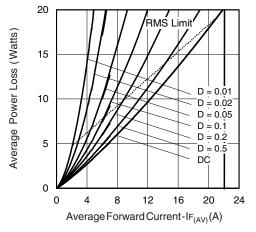


Fig. 6 - Forward Power Loss Characteristics

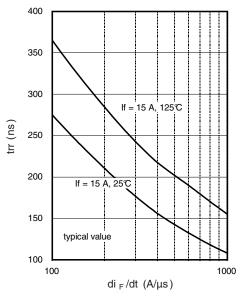


Fig. 7 - Typical Reverse Recovery vs. dl<sub>F</sub>/dt

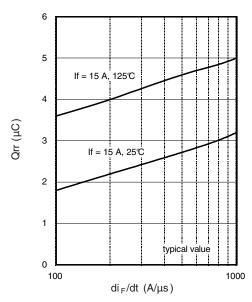
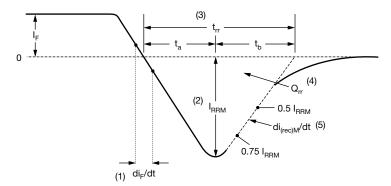


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



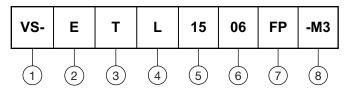


- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (4)  $Q_{rr}$  area under curve defined by  $t_{rr}$  and  $l_{RRM}$
- (2)  $I_{RRM}$  peak reverse recovery current
- $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RBM</sub> and 0.50 I<sub>RBM</sub> extrapolated to zero current.
- (5) di<sub>(rec)M</sub>/dt peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 9 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Circuit configuration:

E = single

**3** - T = TO-220

L = hyperfast recovery time

5 - Current code: 15 = 15 A

- Voltage code: 06 = 600 V

7 - FP = 2L TO-220 FullPAK

8 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

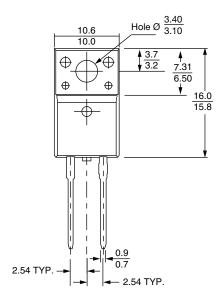
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-ETL1506FP-M3	50	1000	Antistatic plastic tube		

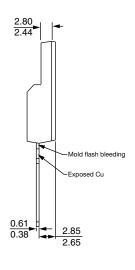
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96157			
Part marking information	www.vishay.com/doc?95392			

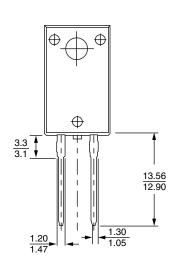


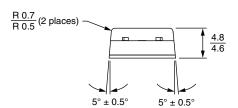
## 2L TO-220 FullPAK

### **DIMENSIONS** in millimeters









Bottom view



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Vishay

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